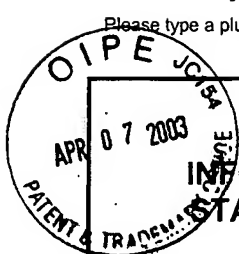


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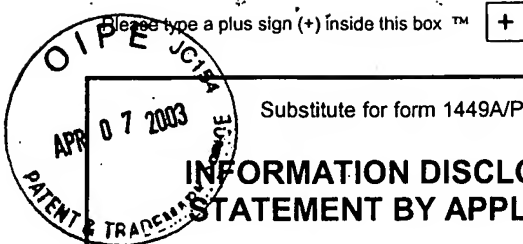
Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application Number	10/062,044	
			Filing Date	1/31/02	
			First Named Inventor	Steven Teig, et al.	
			Group Art Unit	2825	
			Examiner Name	Naum B. Levin	
Sheet	1	of	1	Attorney Docket Number	SPLX.P0093

RELATED U.S. PATENT APPLICATIONS					
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		Serial Number			
NL	1.	10/062,017	Steven Teig & Asmus Hetzel	01-31-02	
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NL	3.	10/062,014	Steven Teig & Asmus Hetzel	01-31-02	
NL	4.	10/066,264	Steven Teig & Asmuz Hetzel	01-31-02	
NL	5.	10/066,188	Steven Teig & Asmus Hetzel	01-31-02	
NL	6.	10/062,992	Steven Teig & Asmus Hetzel	01-31-02	
NL	7.	10/061,474	Steven Teig & Asmuz Hetzel	01-31-02	
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NL	9.	10/066,456	Steven Teig & Asmus Hetzel	01-31-02	
NL	10.	10/062,993	Steven Teig & Asmus Hetzel	01-31-02	
NL	11.	10/062,047	Steven Teig & Asmuz Hetzel	01-31-02	

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Sheet	2	of	2	Attorney Docket Number	SPLX.P0093

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner* Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁶
NL	12.	D. Jongeneel, R. Otten, Y. Watanabe and R. K. Brayton, Area and Search Space Control for Technology Mapping, 37 th Design Automation Conference, 86-91, 2000.	
NL	13.	Henrik Reif Andersen, An Introduction to Binary Decision Diagrams, October 1997 (minor revisions April 1998), 36 pp.	
NL	14.	Jerry Burch and David Long, Efficient Boolean Function Matching, Proc. ICCAD 1992, 408-411.	
NL	15.	John Fishburn, A Depth-Decreasing Heuristic for Combinational Logic; or How to Convert Ripple-Carry Adder into a Carry-Lookahead Adder or Anything In-Between, Proceedings of the 27 th Design Automation Conference, 361-364, 1990.	
NL	16.	Kamal Chaudhary and Massoud Pedram, A Near Optimal Algorithm for Technology Mapping Minimizing Area under Delay Constraints, Proceedings of the 29 th Design Automation Conference, 492-498, 1992.	
NL	17.	Kurt Keutzer, DAGON: Technology Binding and Local Optimization by DAG Matching, Proceedings of the 24 th Design Automation Conference, 341-347, 1987	
NL	18.	Randal Bryant, Symbolic Boolean Manipulation with Ordered Binary Decision Diagrams, CMU CS Tech Report CMU-CS-92-160.	
NL	19.	Uwe Hinsberger and Reiner Kolla, Boolean Matching for Large Libraries, Proceedings of the 35 th Design Automation Conference, 206-211, June 1998.	
NL	20.	Yuji Kukimoto, Robert K. Brayton, and Prashant Sawkar, Delay-Optimal Technology Mapping by DAG Covering, Dept. of Electrical Engineering and Computer Science, University of California, Berkeley, Strategic CAD Laboratories, Intel Corp., October 1997.	
NL	21.	Zbigniew J. Czech, et al., An Optimal Algorithm for Generating Minimal Perfect Hashing Functions, Information Processing Letters, 43(5): 257-264, October 1992	

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